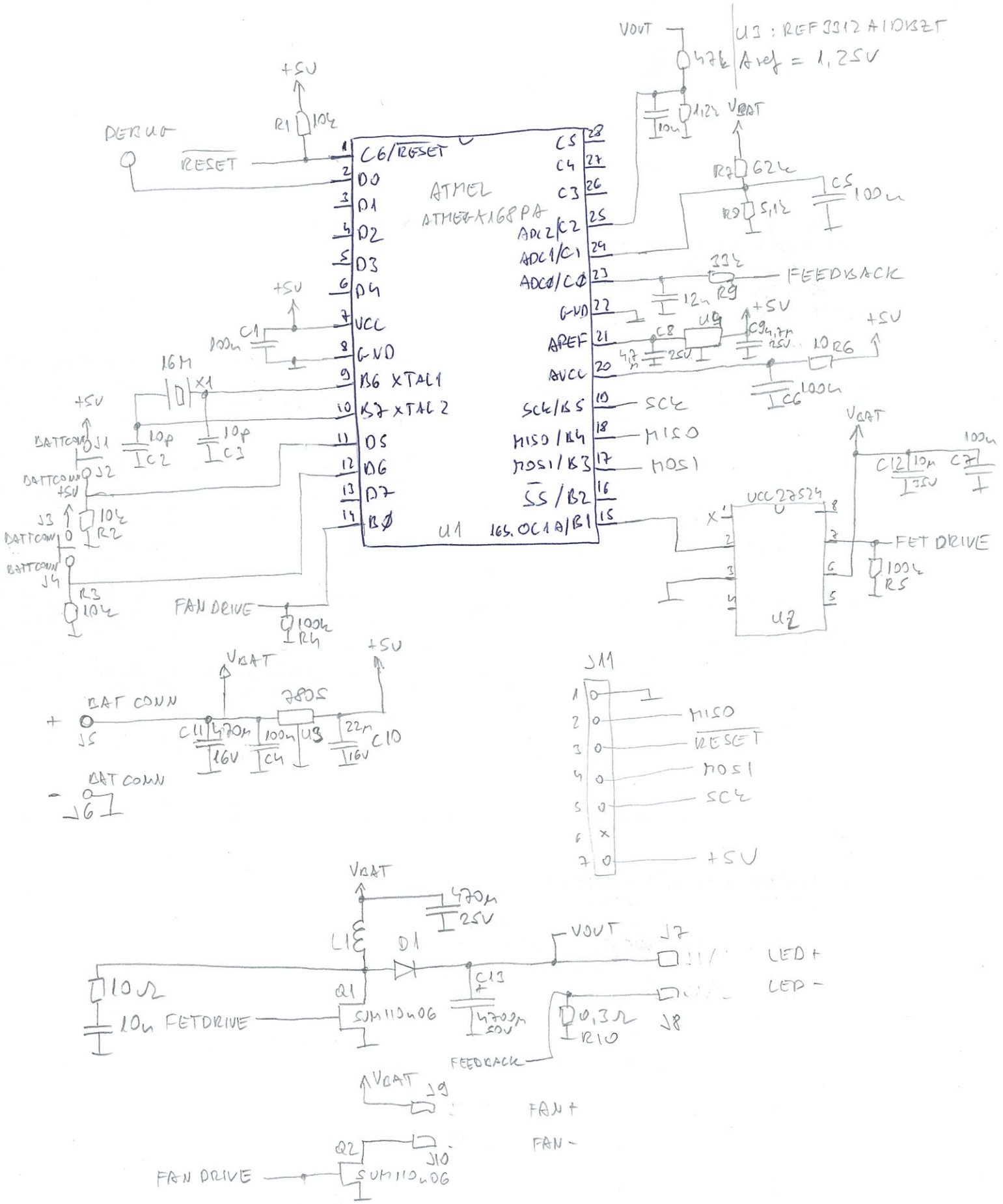
U3 : REF3312AIDBZT
Aref = 1,25V
VOUT
47k
10n
1,2k
VBAT
R7 62k
R8 5,1k
C5 100n
+5V
R1 10k
DEBUG
RESET
ATMEL
ATMEGA168PA
U1
C6/RESET
D0
D1
D2
D3
D4
VCC
GND
B6 XTAL1
B7 XTAL2
D5
D6
D7
B0
C5
C4
C3
ADC2/C2
ADC1/C1
ADC0/C0
GND
AREF
AVCC
SCK/B5
MISO/B4
MOSI/B3
SS/B2
16B.OC1A/B1
33k
R9
FEEDBACK
12n
C8
U4
C9 4,7n 25V
10 R6
C6 100n
C1 100n
16M
X1
10p C2
10p C3
BATTCONN J1
BATTCONN J2
J3
10k R2
BATTCONN J4
R3 10k
FAN DRIVE
100k R4
SCK
MISO
MOSI
VBAT
C12 10n 25V
C7 100n
UCC27524
U2
FET DRIVE
100k R5
BAT CONN
J5
C11 470n 16V
7805
U3
C4 100n
C10 22n 16V
BAT CONN
J6
J11
MISO
RESET
MOSI
SCK
+5V
VBAT
470n 25V
L1
D1
VOUT
J7
LED+
LED-
10Ω
10n
FETDRIVE
Q1
SUM110N06
C13 4700n 50V
0,3Ω R10
J8
FEEDBACK
VBAT
J9
FAN+
FAN-
Q2
J10
FAN DRIVE
SUM110N06